



FIG. 1

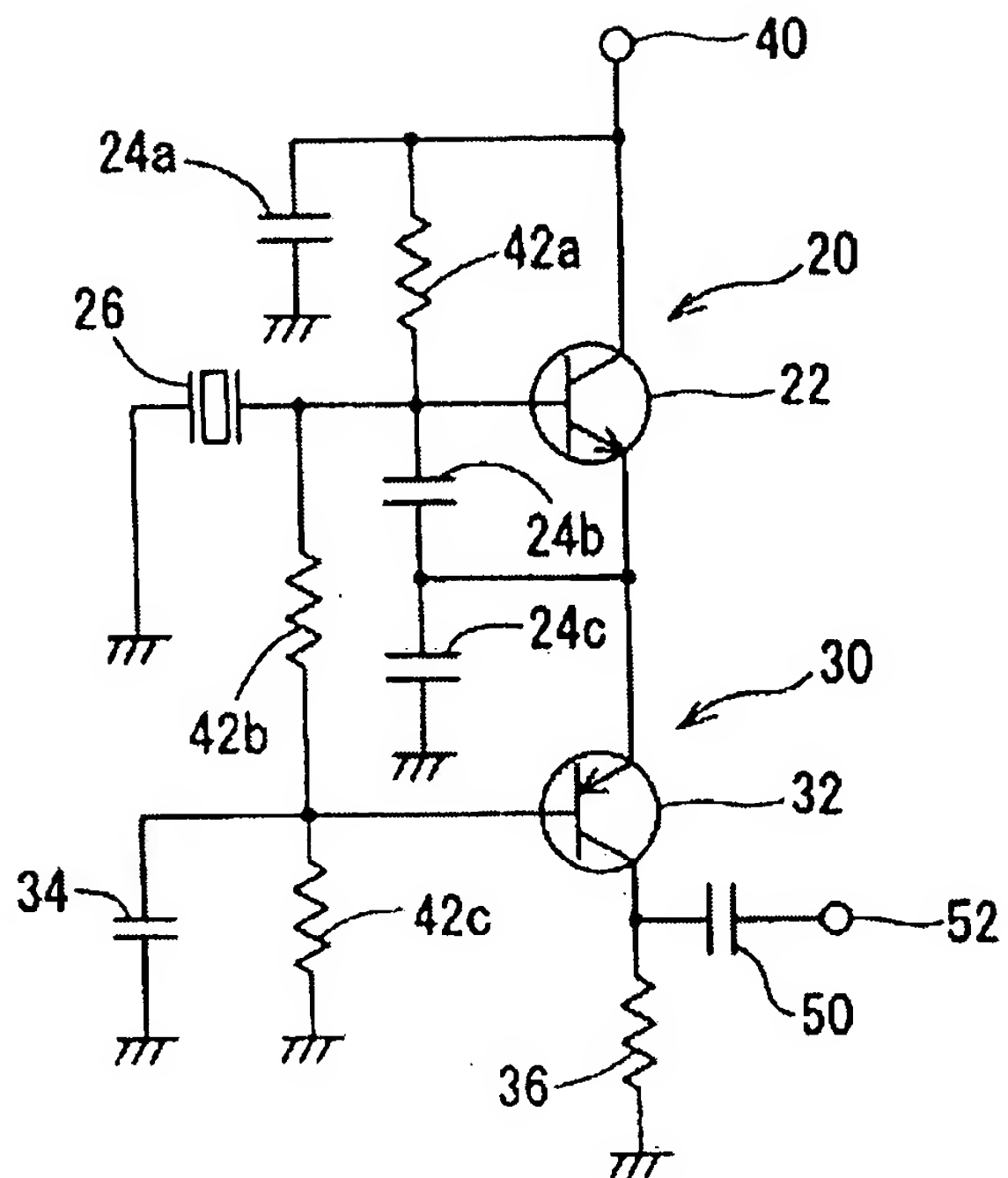




FIG. 2

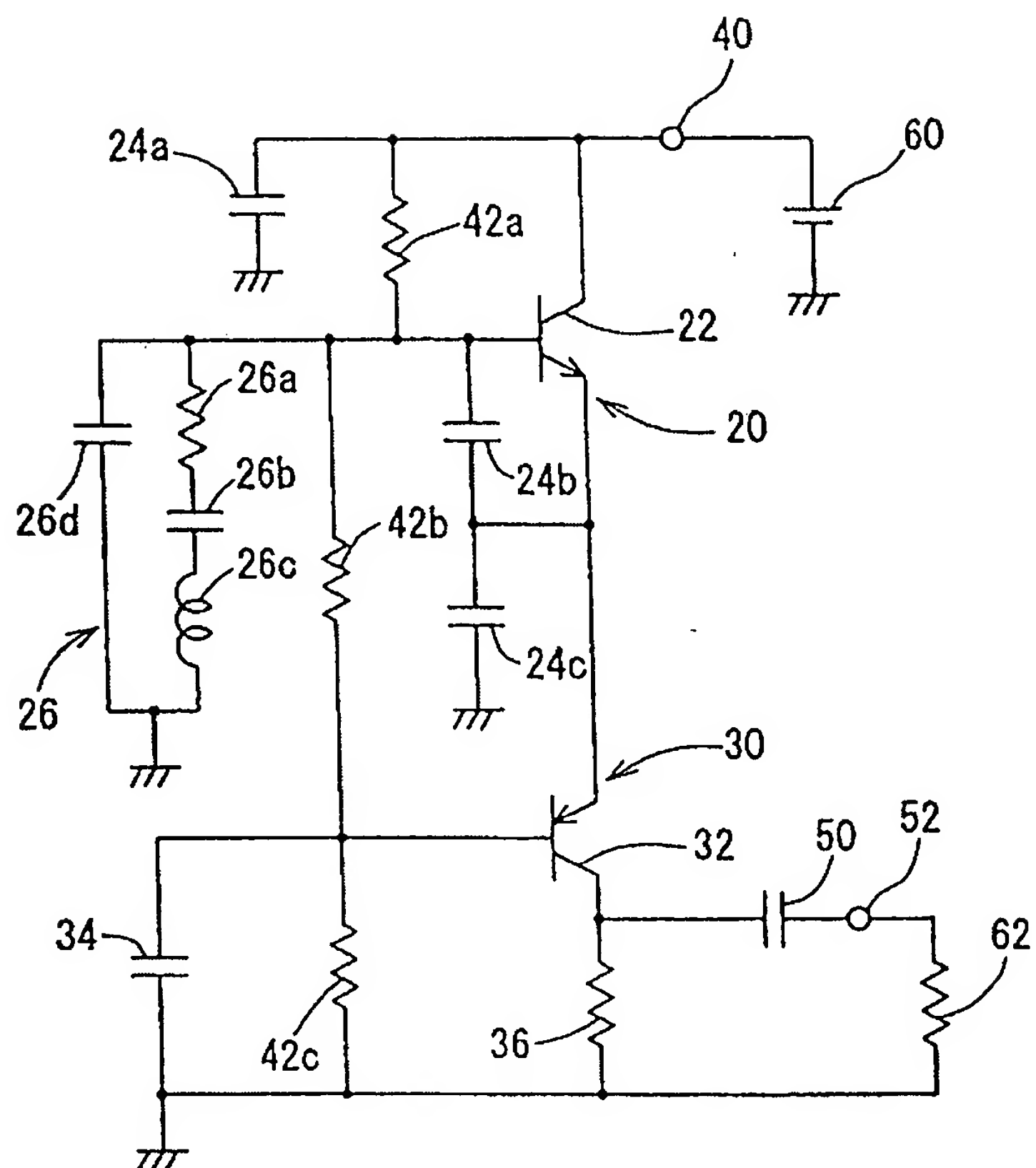
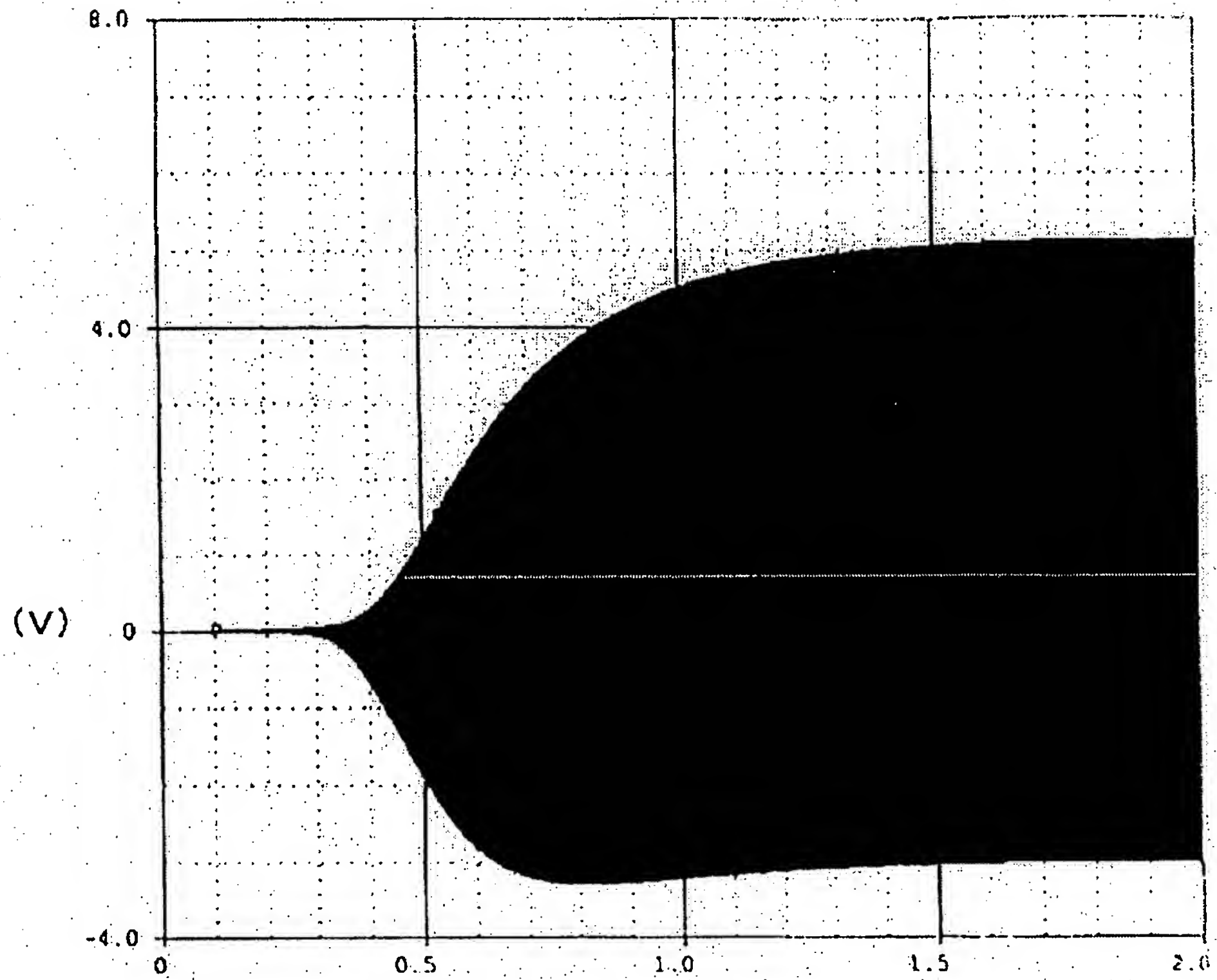




FIG. 3



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FIG. 4

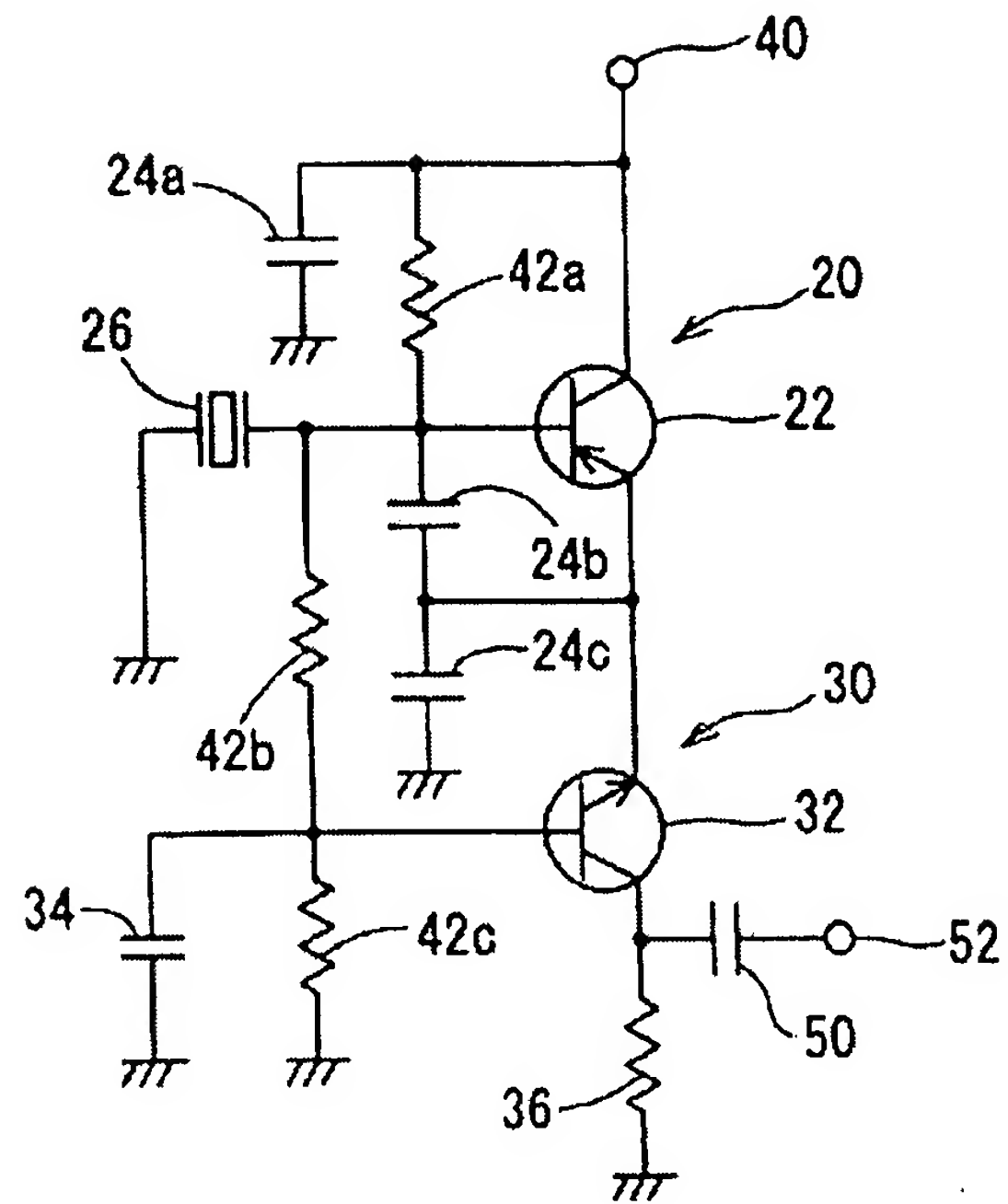




FIG. 5

10

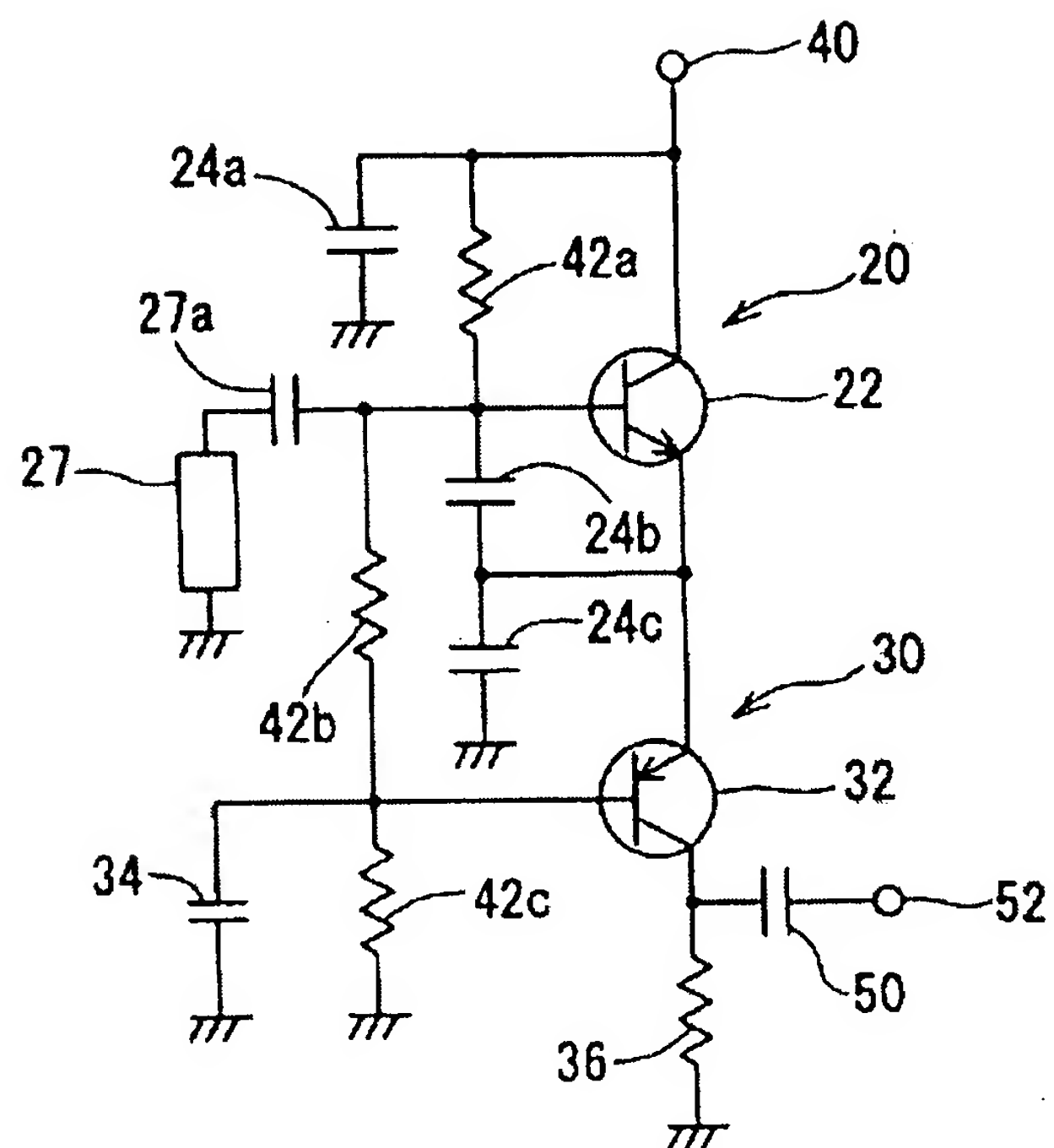
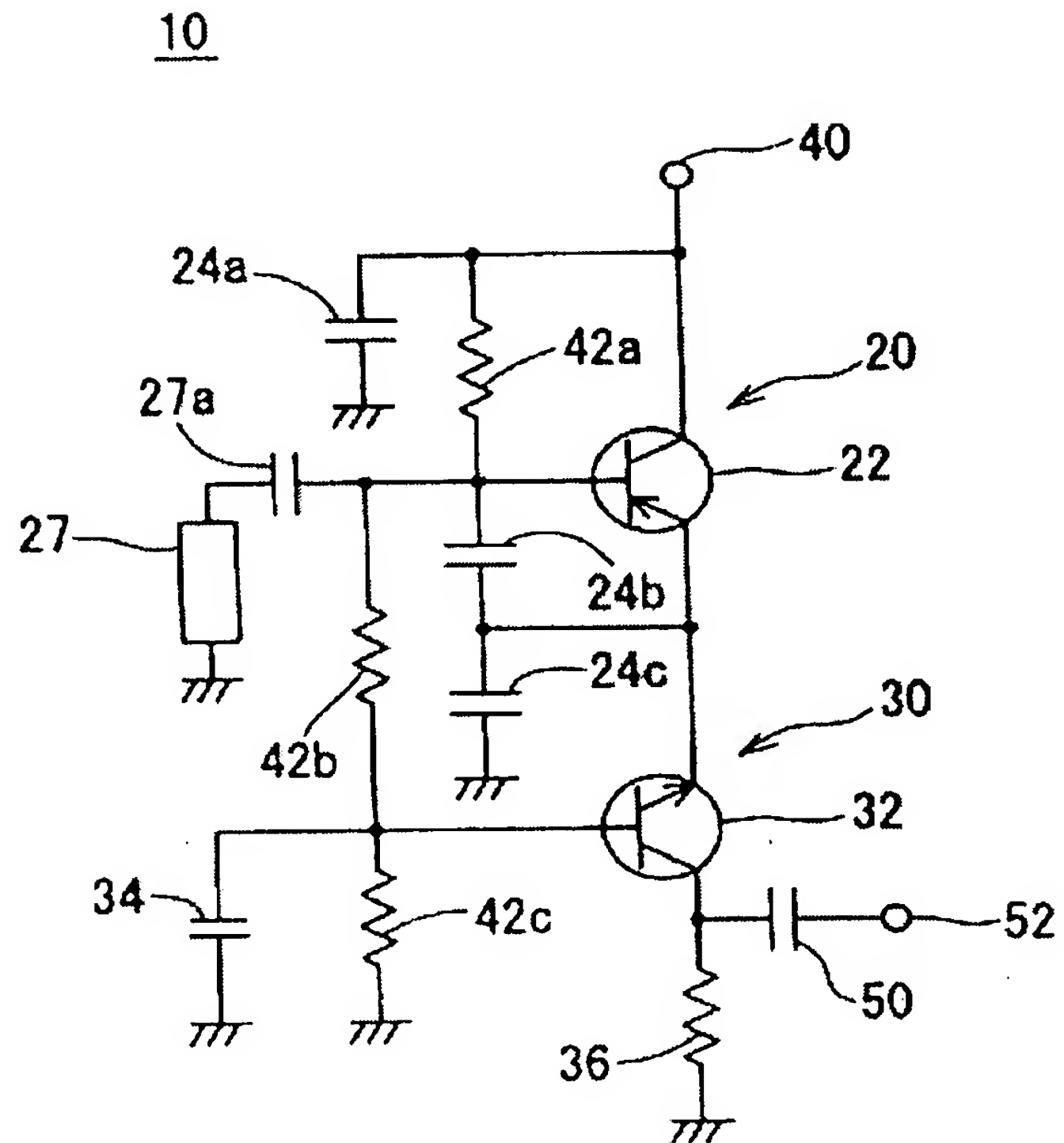




FIG. 6



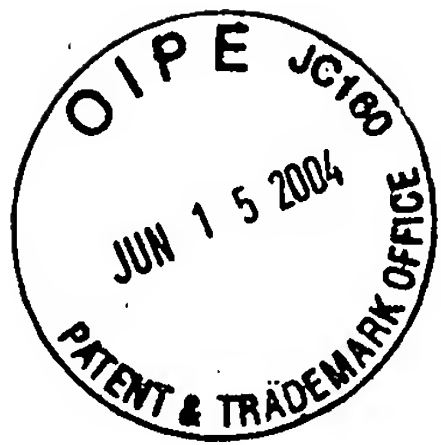


FIG. 7

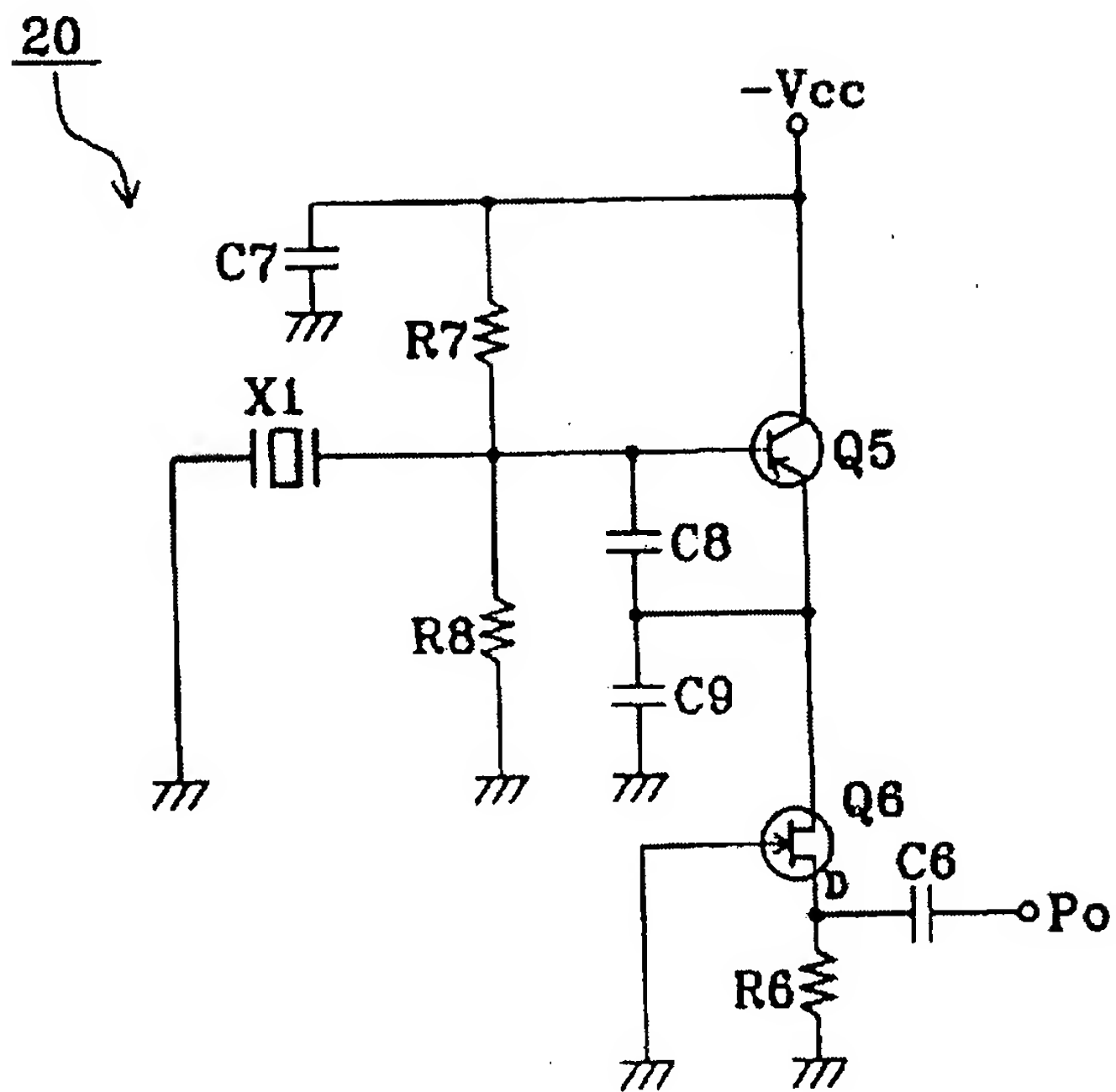
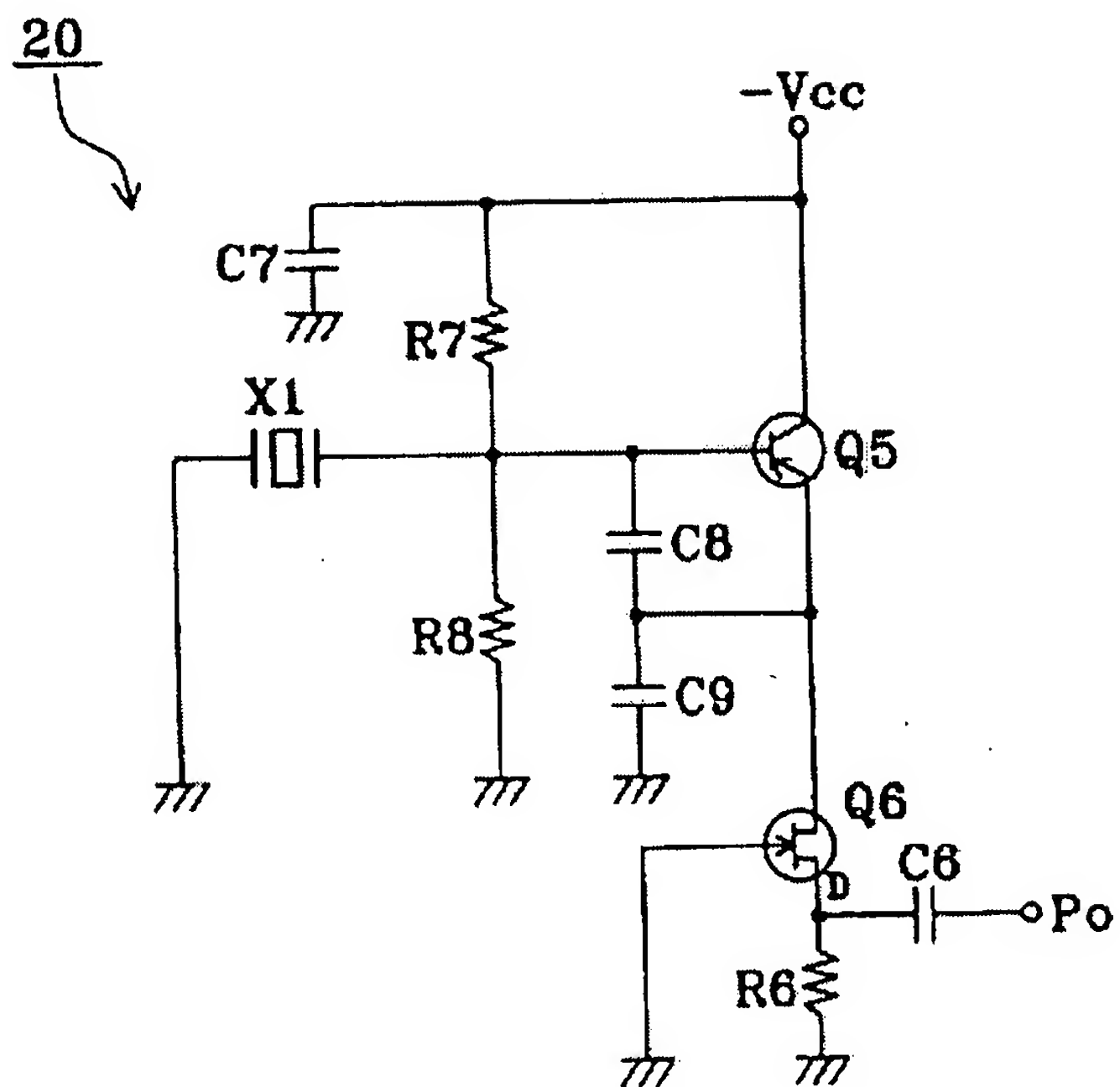


FIG. 8





30

The circuit diagram, labeled 30, shows a Class AB push-pull amplifier. It features two NPN transistors, Q3 and Q4. The base of Q3 is biased using a voltage divider consisting of resistors R7 and R8 connected to +Vcc and ground, with a parallel combination of capacitors C7 and C8. The base of Q4 is biased using a similar voltage divider with resistors R6 and R8, and capacitors C8 and C9. The emitters of both Q3 and Q4 are connected to a common emitter point, which is connected to ground through resistor R6. The collector of Q3 is connected to +Vcc through a load network consisting of inductor L2 in series with a parallel combination of capacitor C10 and inductor L1. The collector of Q4 is connected to the output terminal Po through capacitor C6. A diode VD is connected in parallel with C10, with its cathode to ground and anode to the collector of Q3. Inductor L1 is connected between the collector of Q3 and the common emitter point. A label '30' with a curved arrow points to the overall circuit.

FIG. 10

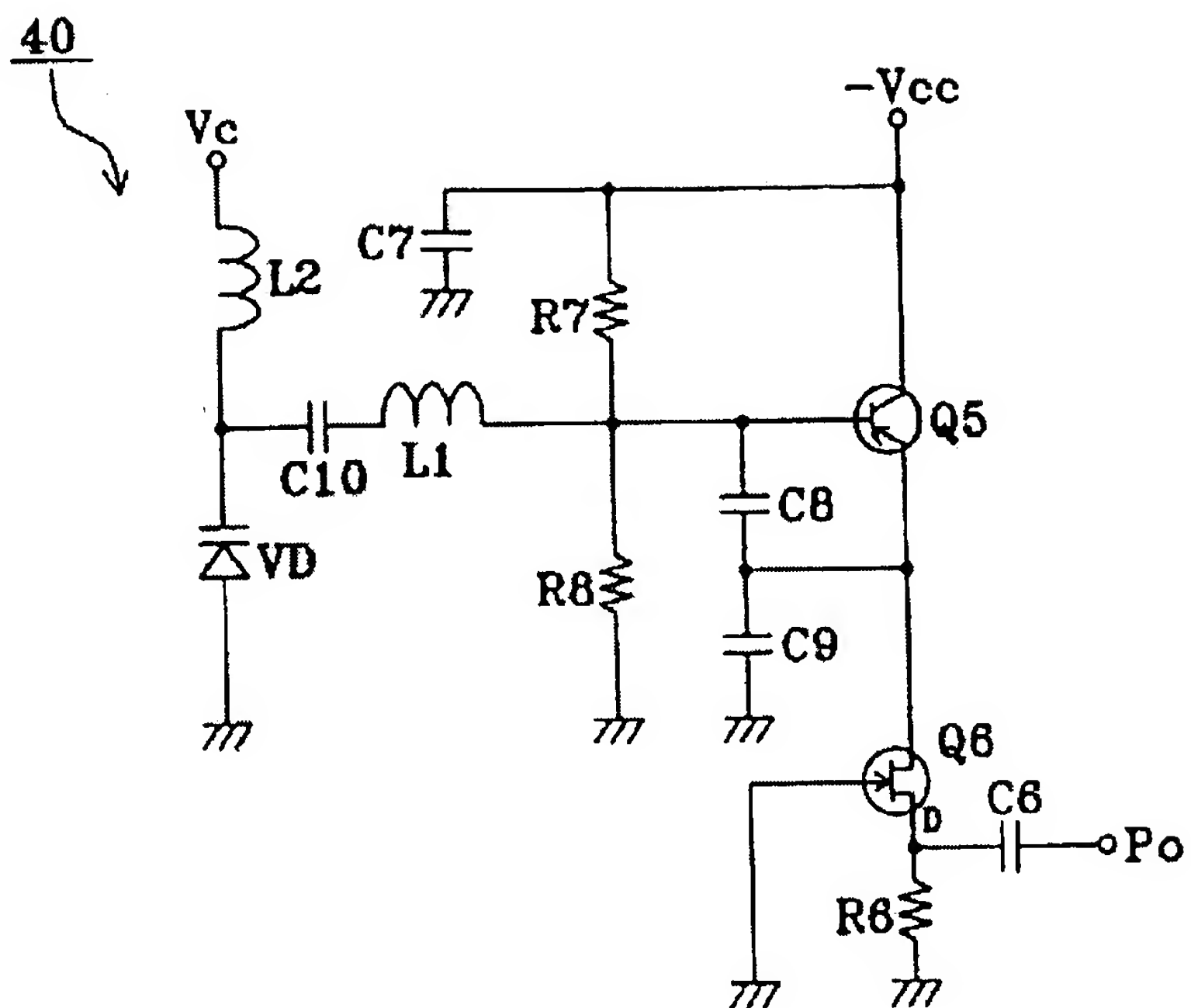




FIG. 11

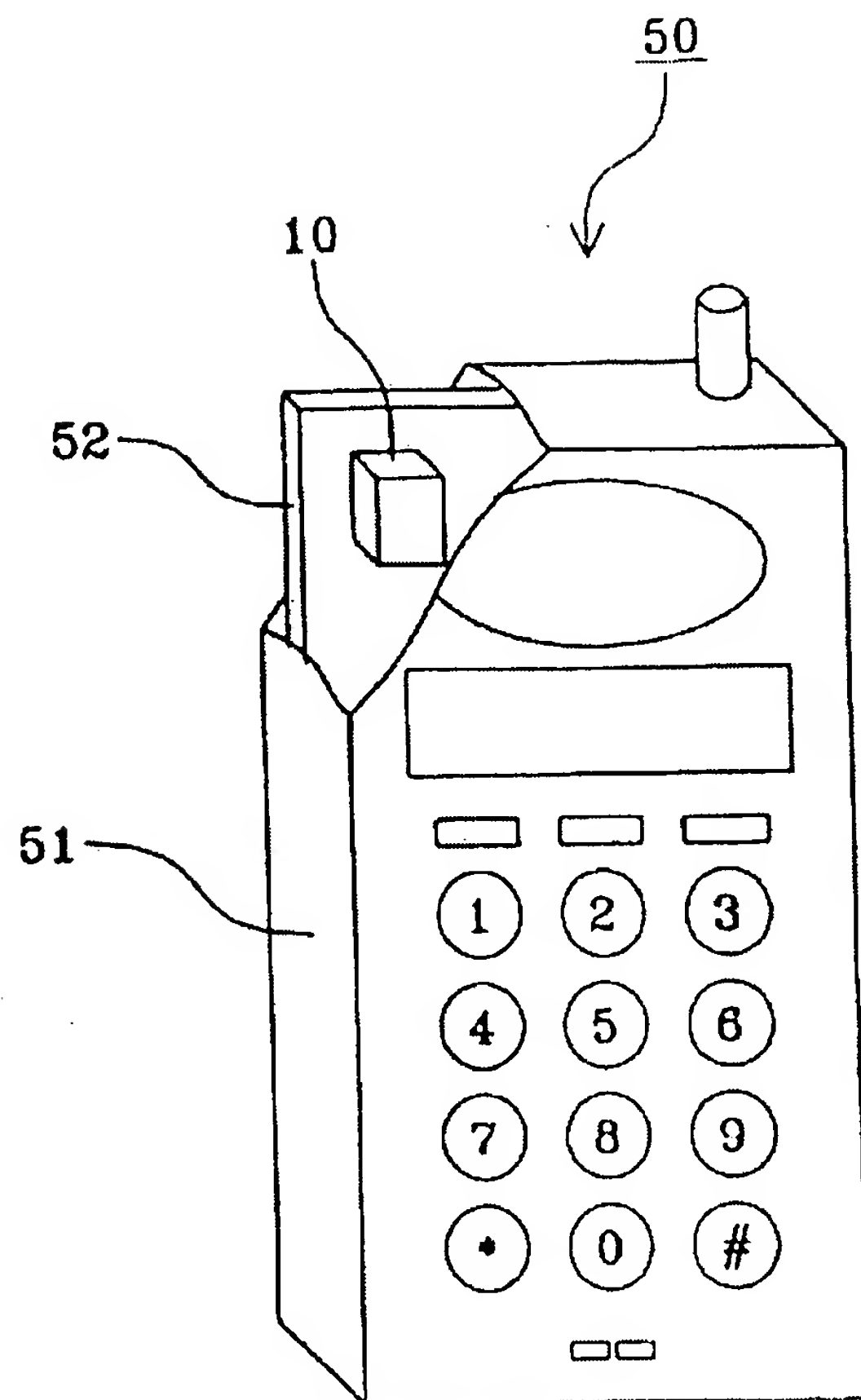




FIG. 12  
PRIOR ART

1

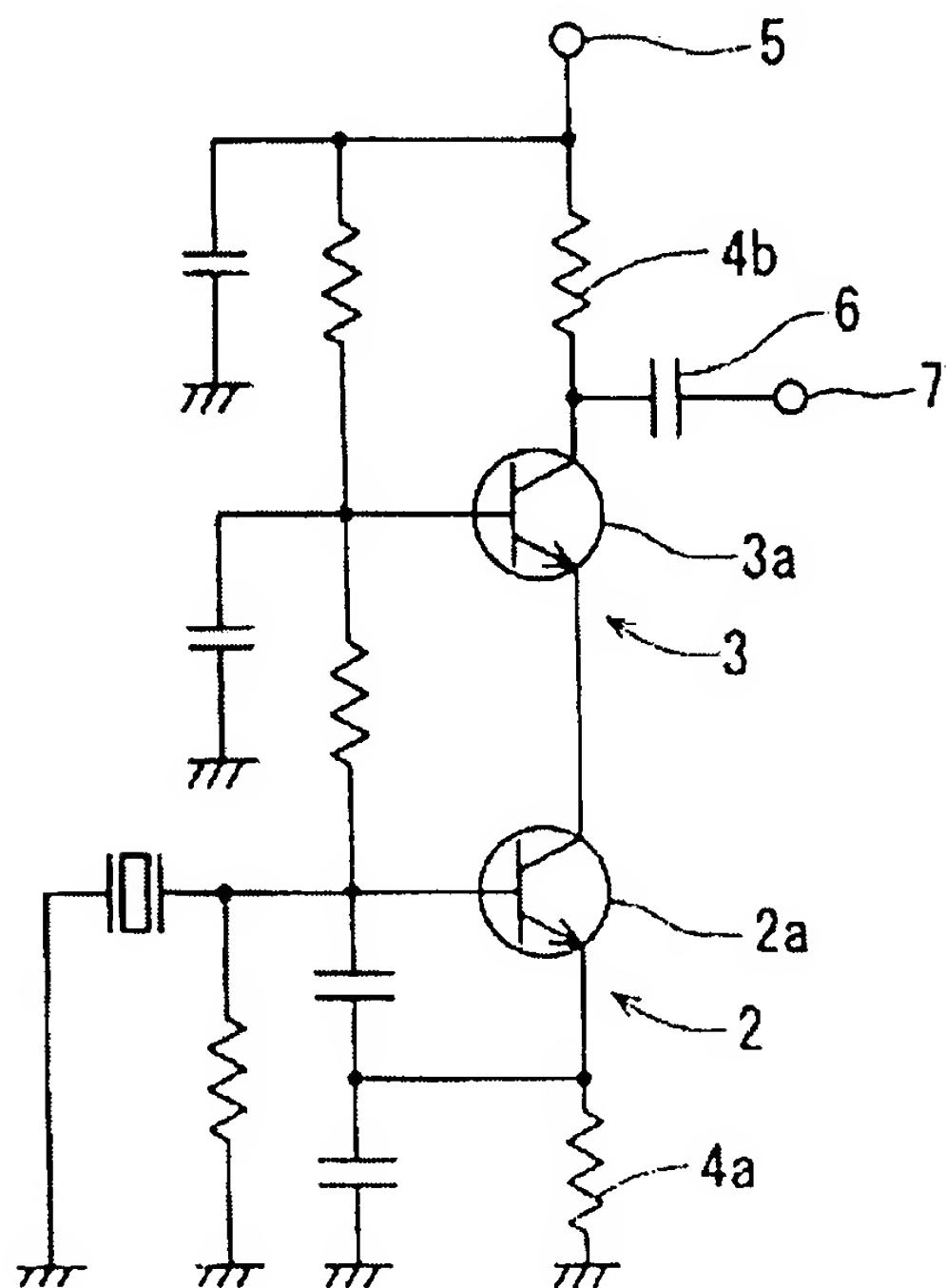


FIG. 13

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